REMARKS

Applicant made amendments to independent claims 1, 9 and 25 to enhance clarity.

Rejections under 35 USC 102/103

To the extent that any art rejection continues to be applied against independent claim 1. 9 or 25, as amended, reconsideration is requested.

Applicant believes neither Fujioka nor DeLuca, whether or not applied, either singly or in combination, discloses, teaches or suggests "a variable voltage regulator...supply an operating voltage to said integrated circuit in response to and representative of said voltage control information", let alone such feature in combination with the remaining elements of either claim 1, 9 or 25, all as amended.

In claim 1 (as amended), the minimum voltage information stored in memory is used to set the operating voltage of the circuit by adjusting the output voltage of a variable voltage regulator (e.g., see \$\infty\$0041-42 of the present invention). The present invention does not adjust or otherwise modify the tailored voltage information after it has been finally determined and stored in memory. Thus, unlike Fujioka, the present invention uses stored voltage control information to set the output voltage of a variable regulator, the voltage control information being tailored to a particular IC, i.e. representative of the stored voltage control information. The present invention does not use the voltage control information as a starting point for modifying or otherwise replacing this information if deemed ineffective.

Entry and allowance are solicited.

Respectfully Submitted, Mark Bilak

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